PhD Preliminary Written Exam	Problem 9	Page 1 of 2
Spring 2014	Power Electronics	Solution

Problem 9 (a) A buck converter is to be designed to deliver power from a DC input with voltage 12 V to an output of 5 V. The switching frequency is chosen to be f = 25 kHz. The specifications call for a 20 mV peak-to-peak output-voltage ripple, and a 0.8 A peak-to-peak inductor-current ripple. Assume all switching and filter components are ideal.

- (i) What is the duty cycle that the converter should operate at?
- (ii) What value of filter inductance would meet the specifications?
- (iii) What value of filter capacitance would meet the specifications?
- (iv) Assuming a load resistance, $R = 500 \Omega$, what is the critical filter inductance for the converter? (Recall, the converter operates at the boundary of continuous- and discontinuous-current conduction modes when the inductance is chosen to be the *critical filter inductance*.)

Solution 9 (a)

(i) The input voltage, $V_{in} = 12$ V; and output voltage, $V_{out} = 5$ V. The duty cycle, D, is given by

$$D = \frac{V_{\text{out}}}{V_{\text{in}}} = \frac{5}{12} \approx 41.67\%.$$
 (1)

(ii) With the active switch turned on, we can write

$$V_{\rm in} - V_{\rm out} = L \frac{di}{dt},\tag{2}$$

where i denotes the instantaneous inductor current. With a straight-line approximation for the inductor current, we get

$$V_{\rm in} - V_{\rm out} \approx L \frac{\Delta i}{DT},$$
(3)

where $T = f^{-1}$ is the switching period and Δi is the current ripple. Substituting the specifications of the converter and the duty cycle from (1),

$$L = \frac{(V_{\rm in} - V_{\rm out})D}{\Delta i \cdot f} = \frac{(12 - 5)5}{12 \times 0.8 \times 25 \times 10^3} \approx 146\,\mu\text{H}.$$
 (4)

(iii) The instantaneous capacitor current is given by

$$i = C \frac{dv}{dt} \tag{5}$$

Since the average capacitor current is zero, assuming the inductor ripple current is completely absorbed by the capacitor, we can write

$$\int_{i_{\rm ripple}>0} i_{\rm ripple} dt = \frac{1}{2} \times \frac{T}{2} \times \frac{\Delta i}{2} = C\Delta v \tag{6}$$

Substituting the values of the allowed current ripple Δi and the allowed voltage ripple Δv , we get

$$C = \frac{T \times \Delta i}{8 \times \Delta v} = \frac{0.8}{8 \times 25 \times 10^3 \times 20 \times 10^{-3}} = 200 \,\mu\text{F}.$$
 (7)

(iv) Denote the critical inductance of the dc-dc buck converter by $L_{\rm crit}$. Recall that the critical inductance is the minimum inductance required to avoid discontinuous conduction mode (DCM) under all operating conditions. That is, if the chosen inductor for the dc-dc buck converter, $L > L_{\rm crit}$, then DCM is avoided. On the other hand, if the dc-dc buck converter inductor $L < L_{\rm crit}$, then the converter always operates in DCM. For $L = L_{\rm crit}$, $\Delta i = 2I_{\rm out}$, where $I_{\rm out} = V_{\rm out}/R$ is the average output current. With this operating mode in mind, we get

$$L_{\rm crit} = \frac{(V_{\rm in} - V_{\rm out})D}{\Delta i \cdot f} = \frac{(V_{\rm in} - V_{\rm out})D}{2I_{\rm out} \cdot f} = \frac{(12 - 5)5}{12 \times 2(5/500) \times 25 \times 10^3} \approx 5.83 \,\mathrm{mH.} \quad (8)$$