

Editorial

THE NEW YEAR marks a changing of the guard, as a fresh team takes the helm at the IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS for a two-year term. The new Editorial Board provides a mix of change and continuity, with 12 new Associate Editors joining an experienced group. The members of the Board are familiar and well-respected names in our community, and it is my pleasure to work with them in serving our readers and authors. Collectively, the new Board reflects a broad diversity that is appropriate to our changing field. I am pleased to announce that Prof. Vijay Narayanan from Penn State will serve as the Deputy Editor-in-Chief of the journal.

On a personal note, this journal has been at the focus of my professional life for the past quarter century. As the incoming Editor-in-Chief, I am honored to lead it on its onward journey, and hope to be worthy of the charge.

I am privileged to be supported by a large extended community whose selfless contributions help maintain the vitality of our journal. First and foremost, our success depends on our authors and readers, for what is a journal without content, or without an audience? Second, we are ably assisted by a large group of reviewers from our professional community who selflessly donate their time and energy to provide high-quality feedback to potential authors. Third, we owe thanks to the talented and highly motivated members of our Editorial Board, who coordinate the review process and interpret complex input, while acting with alacrity and ensuring fairness in the review process. The Editor-in-Chief stands at the bottom of this inverted pyramid, and I am humbled in knowing that I can only perform my task because of the effort, cooperation, and generosity of this large community of volunteers.

The journal is the premier forum for presenting peer-reviewed archival research in the area of electronic design

automation. It is widely read, well cited, and highly respected, and we will work actively to maintain and advance that reputation in the coming years. Today, our review turnaround times, citation counts, and the impact factor are at impressive levels. The credit for this achievement is shared by all of my predecessors, and I would like to take this opportunity to particularly acknowledge the role of the outgoing Editor-in-Chief, Prof. Enrico Macii, in leading the journal over the past four years.

However, our work is far from done. We live in a time when change is the only constant. In a fast-moving field, it is important to “skate to where the puck is going to be.” Our field is active in tackling the challenges of nanometer-scale technologies and large-scale system design and test, and it is poised to play an even larger role as we move toward more deeply scaled technologies and new technologies on nonsilicon substrates. We will continue to develop initiatives that I have helped launch in my prior role as the Deputy Editor-in-Chief, such as the Keynote Paper series and topical special issues, and will embark upon new ones to target growth in new directions.

The IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS is well-placed to play a leading role in the dissemination of cutting-edge research for the next generation of technology. With your help, I am confident that it will. Send in your best research, as you always have. And if you have an idea that can help us improve in any way, don't hesitate to get in touch!

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Sachin S. Sapatnekar (S'86–M'93–F'03) received the B.Tech. degree in Electrical Engineering from the Indian Institute of Technology, Bombay, India, the M.S. degree in Computer Engineering from Syracuse University, Syracuse, NY, and the Ph.D. degree in Electrical Engineering from the University of Illinois, Urbana-Champaign, IL.

From 1992 to 1997, he was on the faculty of the Department of Electrical and Computer Engineering, Iowa State University, Ames, IA. Since 1997, he has been with the University of Minnesota, Minneapolis, MN, where he currently holds the Distinguished McKnight University Professorship and the Henle Professorship in Electrical and Computer Engineering. He is an author/editor of eight books, and has published widely in the area of computer-aided design of VLSI circuits. His current research interests lie in the area of the analysis and optimization of timing, power, layout, and reliability of digital circuits.

Dr. Sapatnekar currently serves as the General Chair of the ACM/EDAC/IEEE Design Automation Conference, and has served as the Technical Program Co-Chair of the Conference in the past. He has held similar positions for the ACM International Symposium on Physical Design, and for the IEEE/ACM International Workshop on the Specification and Synthesis of Digital Systems (Tau). He has served on the editorial boards of several publications, including this journal, IEEE DESIGN AND TEST OF COMPUTERS, the IEEE TRANSACTIONS ON VERY LARGE SCALE INTEGRATION SYSTEMS, and the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II. He is the recipient of the National Science Foundation CAREER Award, five Best Paper awards at various conferences, and the Semiconductor Research Corporation Technical Excellence Award.