

	Paper Title	Authors	Journal/C onf.	Ref Details	WLAN Band	Technology
1	A Compact Dual-Band Direct-Conversion CMOS Transceiver for 802.11a/b/g WLAN	Xu et al.	ISSCC	2005, Paper 5.4	a/b/g	0.18u CMOS TSMC
2	A Low-Power Dual-Band Triple-Mode WLAN CMOS Transceiver	Maeda et.al.	ISSCC	2005, Paper 5.5	a/b/g	0.18u CMOS
3	A Single-Chip CMOS Transceiver for 802.11a/b/g Wireless LANS	Ahola et. al.	JSSC	Dec 04, pp2250-2258	a/b/g	0.18u CMOS
4	A Single-Chip Dual-Band Tri-Mode CMOS Transceiver for IEEE 802.11a/b/g Wireless LAN	Zargari et. al.	JSSC	Dec 04, pp2239-2249	a/b/g	0.25u CMOS TSMC
5	A Direct-Conversion CMOS Transceiver for the 802.11a/b/g WLAN Standard Using a Cartesian Feedback Transmitter	Perraud el al.	JSSC	Dec 04, pp 2226-2238	a/b/g	0.18u CMOS
6	A Dual-Band 5.15-5.35 GHz, 2.4-2.5 GHz 0.18 um CMOS Transceiver for 802.11 a/b/g Wireless LAN	Vavelidis et al.	JSSC	July 04, pp1180-1184	a/b/g	0.18u CMOS
7	A Highly Integrated Dual-Band Multimode Wireless LAN Transceiver	Zannoth, et al.	JSSC	July 04, pp 1191-1195	a/b/g	0.25u BiCMOS
8	A 2.4GHz Dual-Mode 0.18um CMOS Transceiver for Bluetooth and 802.11b	Cho et al.	JSSC	Nov 04, pp 1916-1926	b/g	0.18u CMOS
9	A 5 GHz Direct-Conversion CMOS Transceiver Utilizing Automatic Frequency Control for the IEEE 802.11a Wireless LAN Standard	Razavi et. al.	JSSC	Dec 03, pp2209-2220	a	0.18u CMOS
10	A 5-GHz Direct Conversion CMOS Transceiver	Zhang et al.	JSSC	Dec 03, pp 2232-2238	a	0.18u CMOS
11	A Single-Chip Digitally Calibrated 5.15-5.825 GHz 0.18um CMOS Transceiver for 802.11a Wireless LAN	Vassiliou et al.	JSSC	Dec 03, pp 2221-2231	a	0.18u CMOS
12	A 5 GHz CMOS Transceiver for IEEE 802.11a Wireless LAN Systems	Zargari et al.	JSSC	Dec 02, pp 1688-1694	a	0.25u CMOS
13	115-mW, 0.5-um CMOS GPS Receiver with Wide Dynamic-Range Active Filters	Derek K. Shaeffer et.al.	JSSC	Dec 98, pp 2219-2232		
14	A 1.5V, 1.5 GHz CMOS Low Noise Amplifier	Derek K. Shaeffer and	JSSC	May 1997, pp. 745-759		